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INFORMATI	ON DISCLOSURE	Application Number	10/613,825		
INFORMATI STATIEMEN MAR 2 7 2007	T BY APPLICANT	Filing Date	7/2/2003		
2 7 2007		First Named Inventor	Robert W. BOESEL		
e MAN 2		Art Unit	2611		
(use smany s	sheets as necessary)	Examiner Name	Ziskand		
Shed PADONS 1	of 1	Attorney Docket Number	029573-0301		

Examiner Cite No.1	Cite		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/JT/	A1	5164959	11-17-1992	Cai et al.	
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UNPUBLISHED U.S. PATENT APPLICATION DOCUMENTS						
Examiner Initials*	Cite No.1	U.S. Patent Application Document Serial Number-Kind Code ² (if known)	Filing Date of Cited Document MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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FOREIGN PATENT DOCUMENTS						
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	NON PATENT LITERATURE DOCUMENTS				
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/JT/	A7	Loeliger, "Probability propagation and decoding in analog VLSI." IEEE Transactions on Information Theory, 47(2): 837-841, 2001.			

Examiner Signature	/Julia Tu/	Date Considered	04/11/2007

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